

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/603,147
 Filing Date June 23, 2000
 Inventor John T. Moore et al.
 Assignee Micron Technology, Inc.
 Group Art Unit 2813
 Examiner Kielin, E.
 Attorney's Docket No. MI22-1443
 Title: Capacitor Constructions, DRAM Constructions and Semiconductive Material Assemblies

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

To: Assistant Commissioner for Patents
 Washington, D.C. 20231

From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
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 Spokane, WA 99201-3828

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OCT 29 2002

TECHNOLOGY CENTER 2800

Dear Sir:

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached.

10/31/2002 CHOLLISH 00000002-280925 10/31/02
 Citation of these references is respectfully requested.

01 FC:1806

180.00 CH

The Commissioner is hereby authorized to charge the amount of \$180.00 to cover the fee specified under 37 C.F.R. § 1.17(p).

Respectfully submitted,

Dated: 10-25-2002

By: Jennifer J. Taylor

Jennifer J. Taylor, Ph.D.
 Reg. No. 48,711

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CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR 1.8 TECHNOLOGY CENTER 2800

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9318 on the date shown below:

1. Certificate of Facsimile Transmission
2. Fee Transmittal
3. Supplemental Information Disclosure Statement w/PTO-1449 and copy of cited reference

Dated: 10-29-2002

By: Jaime M. White
Jaime M. White
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